

D.Voc

Industrial Electronics (East-West Automation) (2023-26)

Fifth Semester (Re-appear)- June, 2026

Subject Name			Linear Integrated Circuit			Programmable Logic Control			Quality Management			Analog Electronics			Transducers &Signal Conditioning			Electronics Instruments & Measurements			Linear Integrated Circuit Lab			Programmable Logic Control Lab			Analog Electronics Lab			Electronics Instruments & Measurements Lab			PCB Assembly Lab			SGP A			
Subject Code			24DECE09			24DECE11			24DMEE01			24DECE13			24DECE15			24DECE16			24DECE10			24DECE12			24DECE14			24DECE17			24DECE18						
Credit			2			2			2			2			2			2			1			1			2			1			3						
Maximum Marks			15	35	50	15	35	50	30	70	100	15	35	50	30	70	100	15	35	50	35	15	50	35	15	50	35	15	50	35	15	50	70	30	100				
Minimum Marks			6	14	20	6	14	20	12	28	40	6	14	20	12	28	40	6	14	20	14	6	20	14	6	20	14	6	20	14	6	20	28	12	40				
S. No.	Roll Number	Student's Name	Theory																		Practical																		
			Int.	Ext.	Grade	Int.	Ext.	Grade	Int.	Ext.	Grade	Int.	Ext.	Grade	Int.	Ext.	Grade	Int.	Ext.	Grade	Int.	Ext.	Grade	Int.	Ext.	Grade	Int.	Ext.	Grade	Int.	Ext.	Grade	Int.	Ext.	Grade				
1	23DPIDE03112	Nishant	12	24	A	11	23	B+	22	42	B+	9	18	C	20	44	B+	12	17	B	17	10	C	28	13	A+	24	10	B+	26	13	A	41	19	B+	6.85			

Date 03/07/2026

Examination Department

*The Results displayed here subject to updation/correction. The final result will on DMC.

*Note: - Result of UMC cases will be final decision as per the of committee after hearing of the cases

Sr.	Abbreviation	Stand for:
1	Note	Marks show in in the column of result means "Pass"
2	RE	Re-appear in the paper indicated in the result column
3	RL_UMC	Result late due to Unfair Means Case
4	RL_D	Result late due to Document
5	RL_A	Result late due to Awards
6	DTN	Detained
7	AB	Absent
8	PC	Paper Cancel